

## EAST Search History

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| S1    | 1874 | 710/22-28.ccls. or 709/212.ccls.  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 08:58 |
| S2    | 182  | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context) near3 (register buffer)) with (count\$3))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 15:42 |
| S3    | 184  | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 09:10 |
| S4    | 11   | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3) with (next near3 (register buffer)))                             | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/05/12 13:21 |
| S5    | 16   | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (next near3 (register buffer pointer)))             | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 15:45 |
| S6    | 0    | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (lookahead near3 (register buffer pointer)))        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 09:18 |
| S7    | 0    | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (look-ahead near3 (register buffer pointer)))       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 09:17 |
| S8    | 0    | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((look adj ahead) near3 (register buffer pointer))) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/11/01 09:17 |

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|     |     |  |   |    |    |                  |
|-----|-----|--|---|----|----|------------------|
| S9  | 0   | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (lookahead))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 09:18 |
| S10 | 23  | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((next link\$4 chain\$4) near3 (register buffer pointer))) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 09:30 |
| S11 | 10  | S1 and (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with ((link\$4 chain\$4) near3 (register buffer pointer)))      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 10:15 |
| S12 | 47  | S1 and (DMA with (prefetch read?ahead look?ahead))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 10:16 |
| S13 | 584 | (DMA with ((setting operation control\$4 tranfer descriptor context) near3 (register buffer)) with (count\$3))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 15:42 |
| S14 | 17  | (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer)) with (count\$3) with (next near3 (register buffer)))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 15:43 |
| S15 | 23  | (DMA with ((setting operation control\$4 tranfer descriptor context configur\$6) near3 (register buffer pointer)) with (count\$3) with (next near3 (register buffer pointer)))                           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/01 15:45 |
| S16 | 720 | (DMA with (select\$4) with (register))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:17 |
| S17 | 135 | (DMA near3 (select\$4) near3 (register))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:22 |

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|     |     |  |   |    |    |                  |
|-----|-----|--|---|----|----|------------------|
| S18 | 3   | (DMA near3 (select\$4) near3 (register)) with (external near3 memory)                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:24 |
| S19 | 21  | (DMA near3 (select\$4) near3 (register)) with memory                                 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:26 |
| S20 | 24  | (DMA near3 (select\$4 mux multiplex\$4) near3 (register)) with memory                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:27 |
| S21 | 332 | (DMA with (select\$4 mux multiplex\$4) with (register)) with memory                  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:27 |
| S22 | 123 | (DMA with (select\$4 mux multiplex\$4) near3 (register)) with memory                 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:27 |
| S23 | 51  | (DMA with (select\$4 mux multiplex\$4) with (register)) with (external near3 memory) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 10:28 |
| S24 | 21  | (DMA with (register) with (transfer near3 condition))                                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:38 |
| S25 | 6   | (DMA with (register) with (transfer near3 condition)) with (memory)                  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:31 |
| S26 | 31  | (DMA with (external near3 memory) with (setting))                                    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:42 |
| S27 | 718 | (DMA with (memory) with (setting))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:42 |

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|     |        |  |   |    |    |                  |
|-----|--------|--|---|----|----|------------------|
| S28 | 110    | (DMA near3 (memory) near3 (setting))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 14:00 |
| S29 | 9      | (DMA near3 (memory) near3 (setting) near3 register)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:49 |
| S30 | 14     | (DMA near3 (memory) near3 (parameter setting) near3 register)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:51 |
| S31 | 12     | (DMA near3 (memory) near3 (instruction) near3 register)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 13:51 |
| S32 | 167    | (DMA with (instruction near3 (memory)) with (transfer))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 14:08 |
| S33 | 20     | (DMA with (instruction near3 (memory)) with (transfer)) with CPU   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/11/02 14:08 |
| S34 | 45     | (DMA with (internal near3 control near3 register))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/12 09:32 |
| S35 | 347447 | ((DMA near3 (controller engine)) or (memory near3 access))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/12 13:22 |
| S36 | 999    | ((DMA near3 (controller engine)) or (memory near3 access)) with (register) with (next subsequent)                                    | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/12 13:23 |
| S37 | 64     | ((DMA near3 (controller engine)) or (memory near3 access)) with (register) with ((next subsequent) near3 (transfer\$4 transmission)) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/12 13:23 |

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|     |       |  |   |    |    |                  |
|-----|-------|--|---|----|----|------------------|
| S38 | 19772 | 710/36-38,52,305,306,316,317.<br>ccls. or 709/238-242.ccls. or<br>370/351-365.ccls.  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 13:24 |
| S39 | 891   | S38 and (server host computer)<br>with (switch router hub) with (I/O<br>peripheral storage)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:24 |
| S40 | 85    | S39 and (switch hub router) with<br>(((input receive\$4 receive\$4 ingress)<br>and (output transmit\$4<br>transmission egress))) with (buffer<br>queue))                                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:31 |
| S41 | 17827 | (server host computer) with<br>(switch router hub) with (I/O<br>peripheral storage)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:02 |
| S42 | 17827 | (server host computer) with<br>(switch router hub) with (I/O<br>peripheral storage)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:24 |
| S43 | 499   | S42 and (switch hub router) with<br>(((input receive\$4 receive\$4 ingress)<br>and (output transmit\$4<br>transmission egress))) with (buffer<br>queue))                                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:25 |
| S44 | 499   | S43 and (switch hub router) with<br>(((input receive\$4 receive\$4 ingress)<br>and (output transmit\$4<br>transmission egress))) with (buffer<br>queue))                                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:34 |
| S45 | 10    | S43 and (switch hub router) with<br>(((input receive\$4 receive\$4 ingress)<br>and (output transmit\$4<br>transmission egress))) with (buffer<br>queue)) with (mux multiplexor<br>multiplexer) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:34 |
| S46 | 13457 | (switch hub router) with (((input<br>receive\$4 receive\$4 ingress) and<br>(output transmit\$4 transmission<br>egress))) with (buffer queue))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:34 |

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|     |     |   |   |    |    |                  |
|-----|-----|---|---|----|----|------------------|
| S47 | 622 | (switch hub router) with (((input receiv\$4 recep\$4 ingress) and (output transmit\$4 transmission egress)) with (buffer queue)) with (mux multiplexor multiplexer) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:34 |
| S48 | 578 | (switch) with (((input receiv\$4 recep\$4 ingress) and (output transmit\$4 transmission egress)) with (buffer queue)) with (mux multiplexor multiplexer)            | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:36 |
| S49 | 550 | S48 and (@ad<="20030818" or @rlad<="20030818")  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:35 |
| S50 | 31  | (switch) near3 (((input receiv\$4 recep\$4 ingress) and (output transmit\$4 transmission egress)) near3 (buffer queue)) with (mux multiplexor multiplexer)          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/15 14:36 |
| S51 | 0   | (initializ\$4 with (map\$4 near3 table) with (switch))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 09:58 |
| S52 | 0   | (initializ\$4 with (map\$4 near3 table))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 09:58 |
| S53 | 194 | (initializ\$4 with (map\$4 near3 table))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 09:58 |
| S54 | 5   | (initializ\$4 with (map\$4 near3 table) with switch)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:01 |
| S55 | 194 | (initializ\$4 with (map\$4 near3 table))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:02 |
| S56 | 6   | (initializ\$4 with (map\$4 near3 table)) same switch  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 16:21 |

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|     |      |   |   |    |    |                  |
|-----|------|---|---|----|----|------------------|
| S57 | 65   | (initializ\$4 with (map\$4 near3 table)) and switch                       | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:13 |
| S59 | 0    | (initializ\$4 with (map\$4 near3 table)) with jtag                        | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:13 |
| S60 | 0    | (initializ\$4 with (map\$4 near3 table)) with IIC                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:14 |
| S61 | 0    | (initializ\$4 with (map\$4 near3 table)) with I2c                         | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:14 |
| S62 | 7    | (map\$4 near3 table) with I2c   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:46 |
| S63 | 4    | (rout\$4 near3 table) with (jtag I2c)                                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:52 |
| S64 | 35   | (server) with (USB) with keyboard with video with mouse                   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:55 |
| S65 | 60   | (host) with (USB) with keyboard with (video display) with mouse           | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 10:55 |
| S66 | 8366 | (switch with (user near3 interface))                                      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 16:21 |
| S67 | 42   | (switch with (user near3 interface)) same ((map\$4 rout\$4 near3 (table)) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 16:27 |

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|     |     |  |   |    |    |                  |
|-----|-----|--|---|----|----|------------------|
| S68 | 276 | access with (switch with (user near3 interface))                     | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 16:28 |
| S69 | 182 | access with (switch with (user near3 interface)) and @ad<="20020818" | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/05/16 16:32 |